

FIG. 1A is a block diagram of a computer system 100. The system 100 includes a CPU 110, ROM 116, RAM 114, I/O adapter 118, communications adapter 134, display adapter 136, and user interface adapter 122. The CPU 110, ROM 116, RAM 114, and I/O adapter 118 are connected to a system bus 112. The I/O adapter 118 is connected to a hard disk 120 and a floppy disk 140. The communications adapter 134 is connected to a network. The display adapter 136 is connected to a monitor 138. The user interface adapter 122 is connected to a keyboard 124, a mouse 126, and a touchpad 132. A speaker 128 is also connected to the system.

100

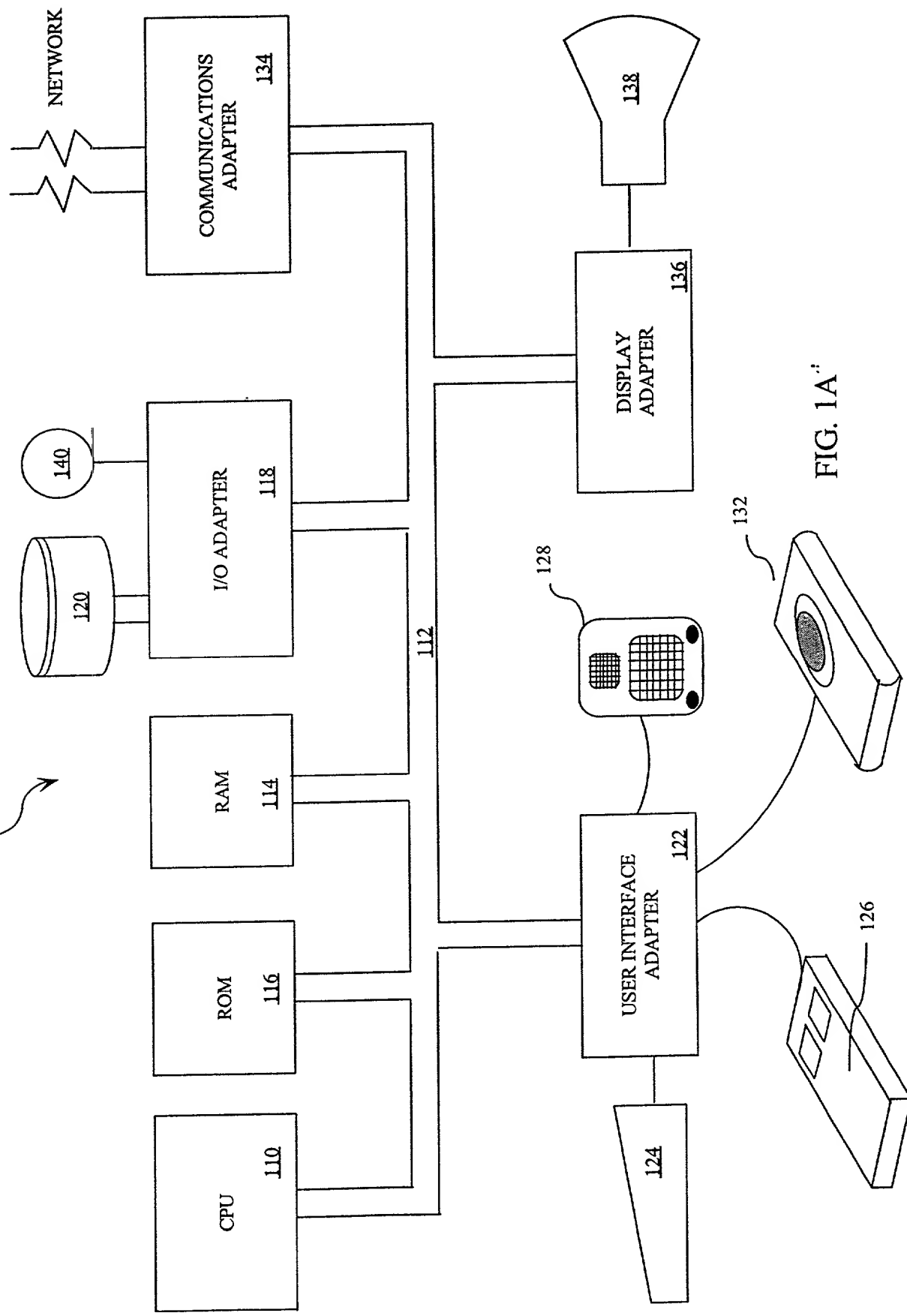


FIG. 1A

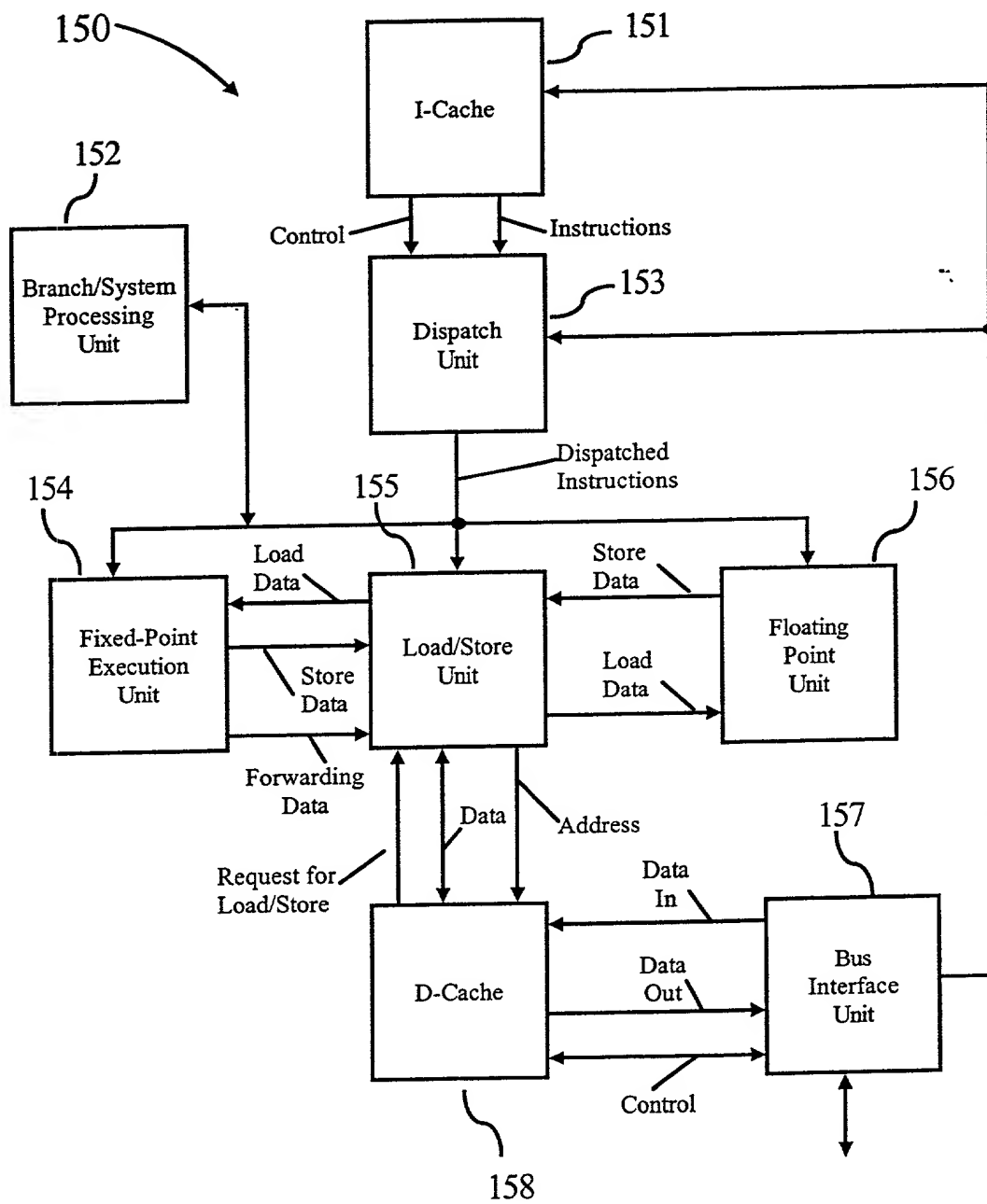


FIG. 1B

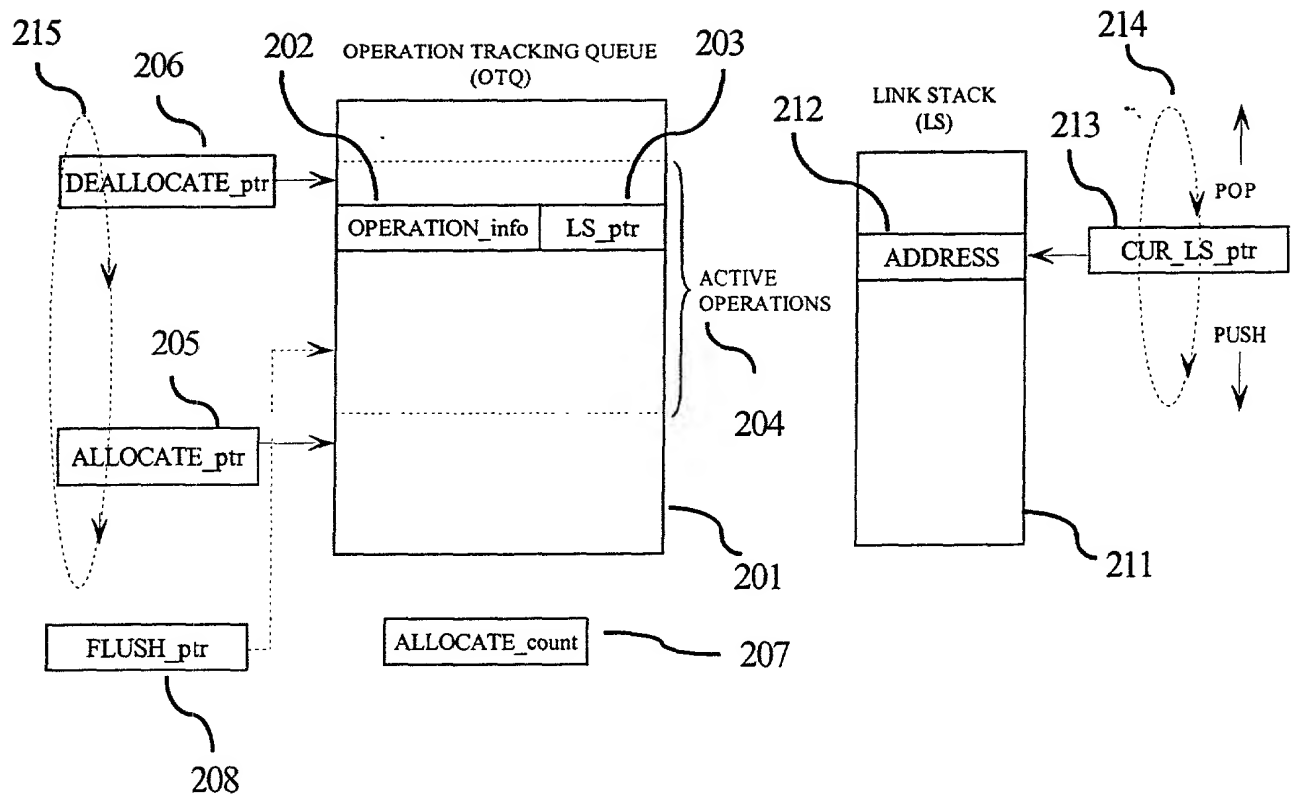


FIG. 2A

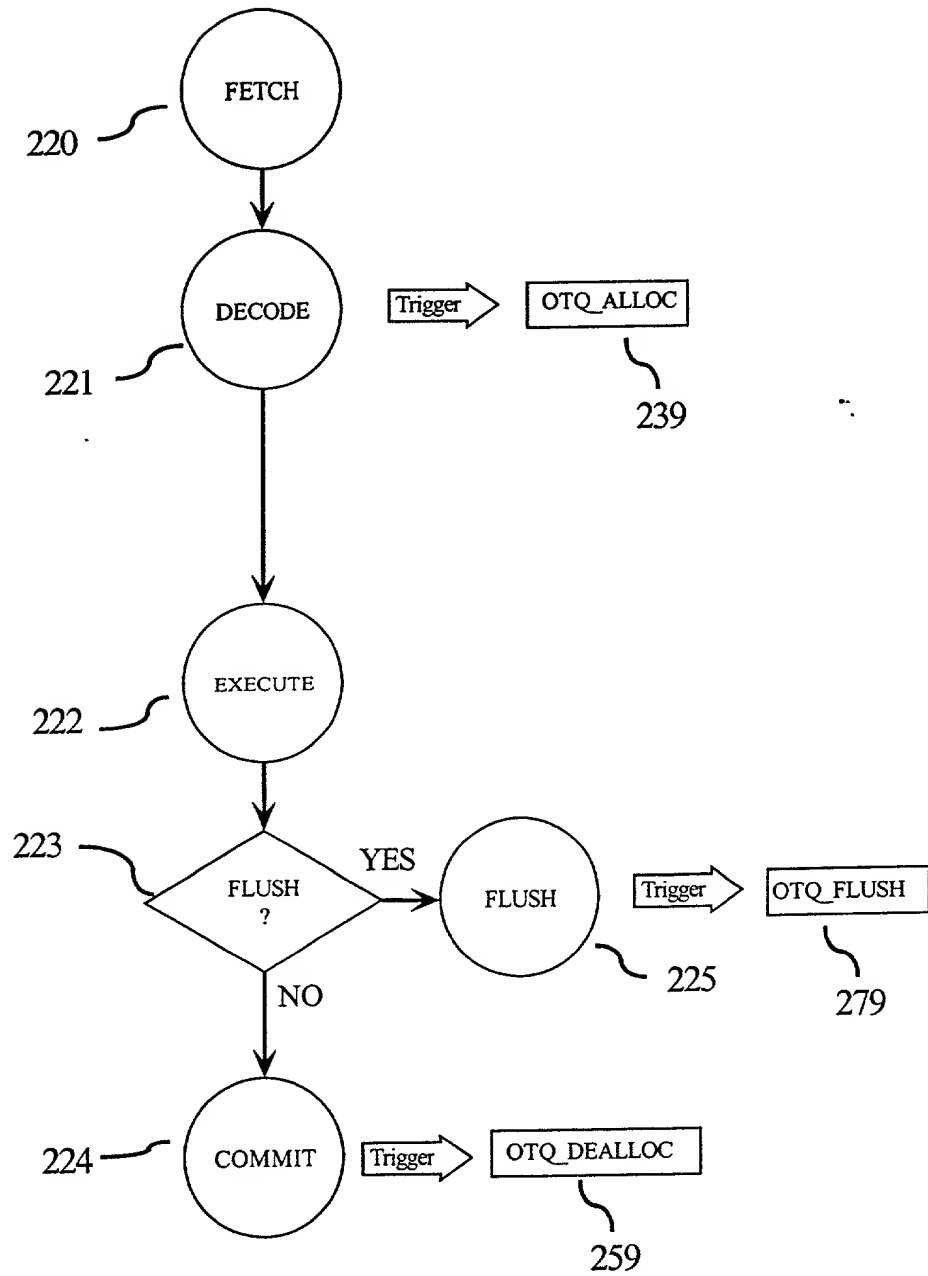


FIG. 2B

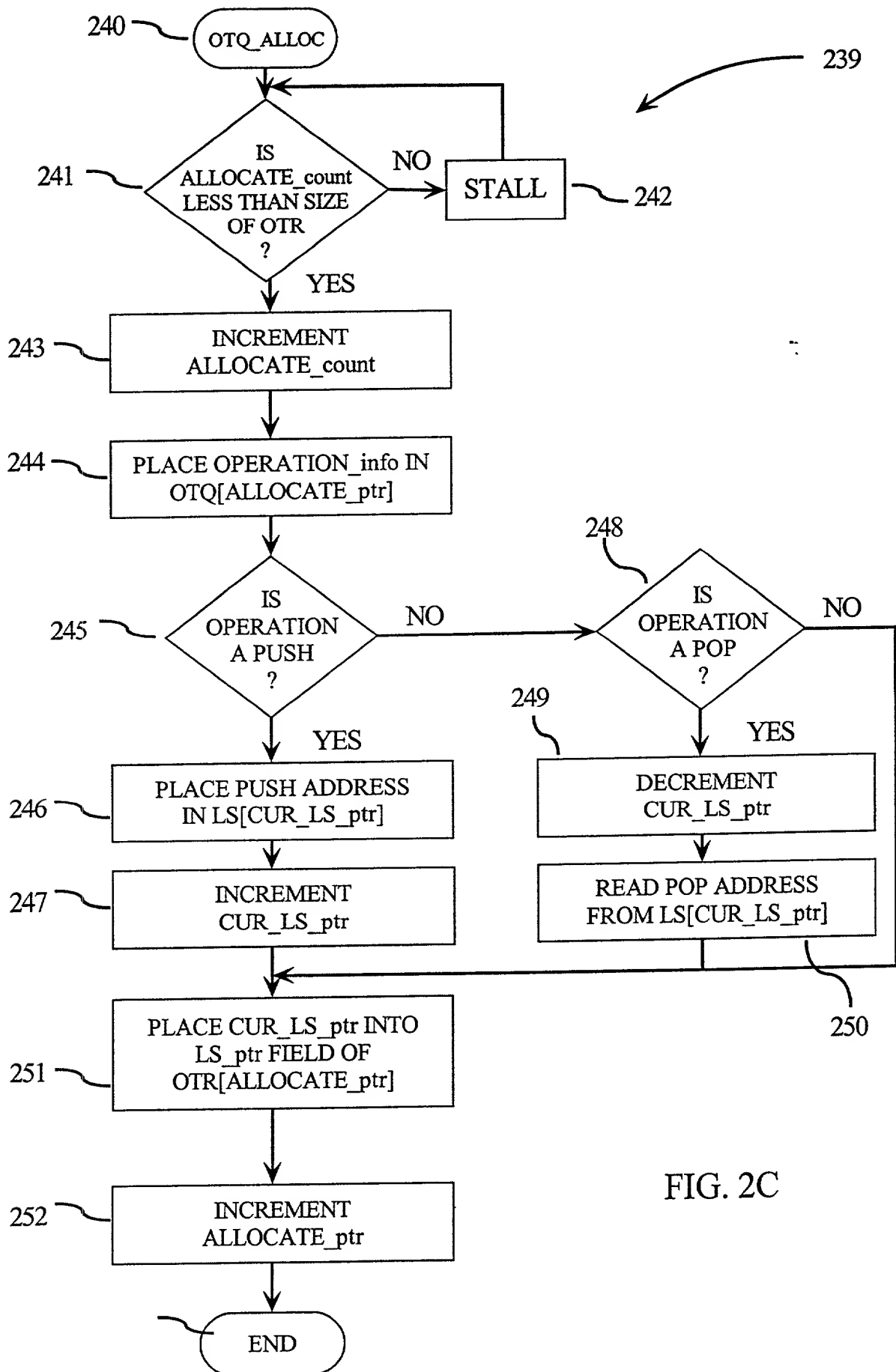


FIG. 2C

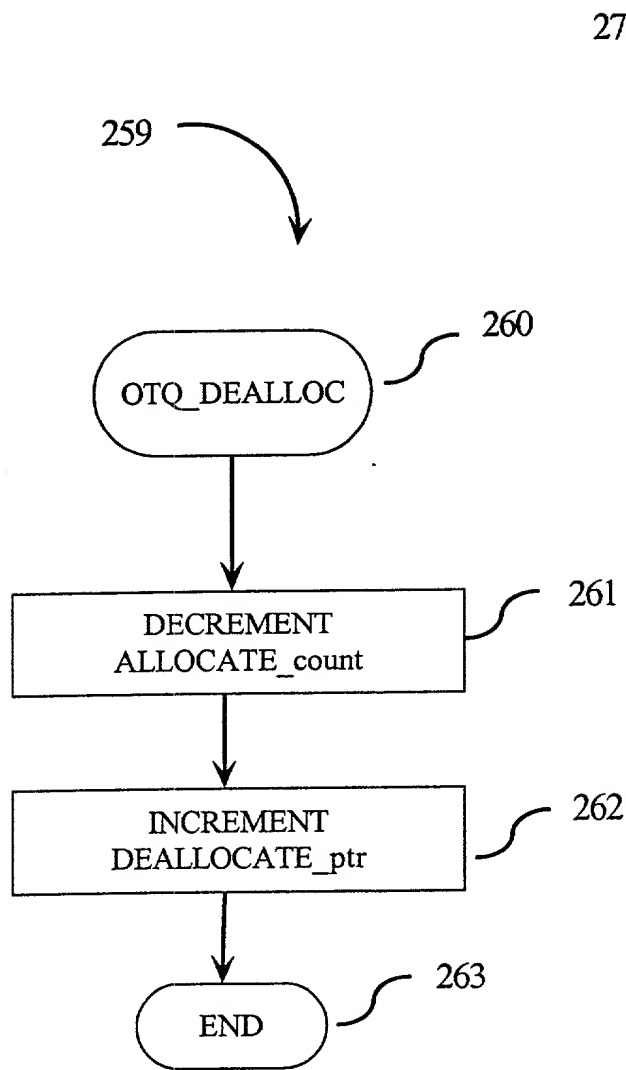


FIG. 2D

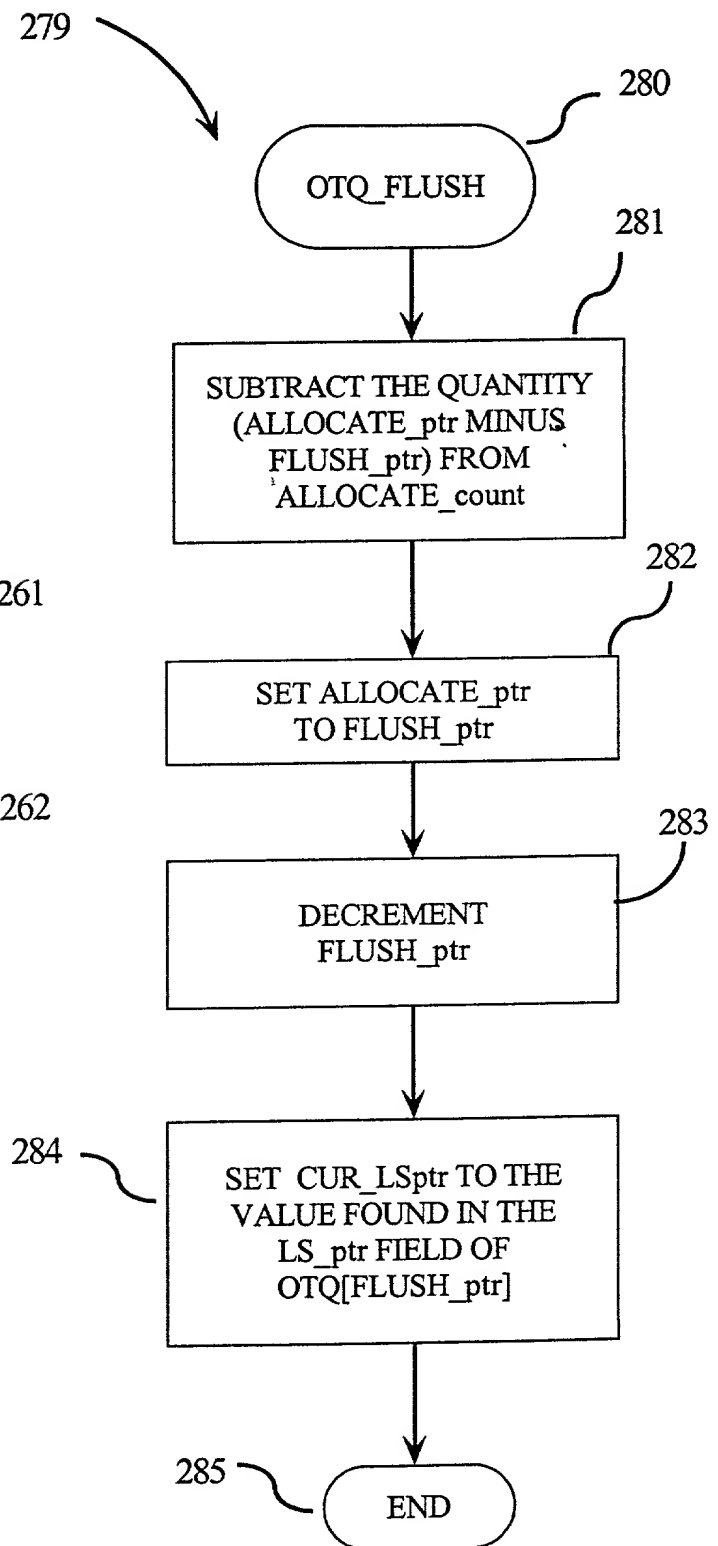


FIG. 2E

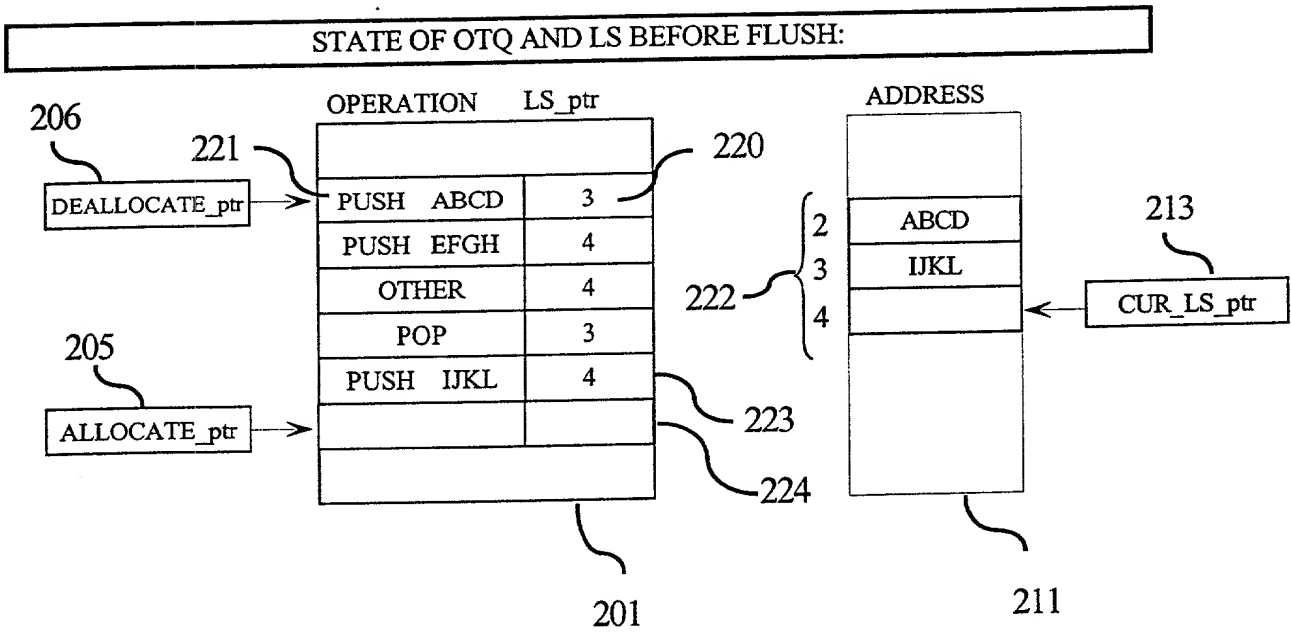


FIG. 2F

STATE OF OTQ AND LS IF ONE ENTRY IS FLUSHED:

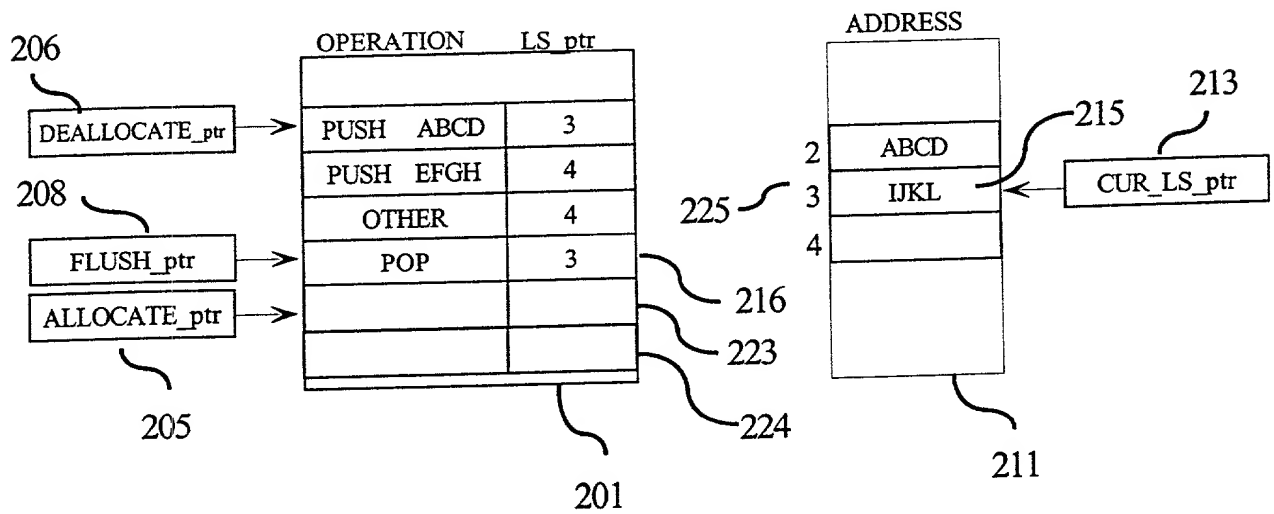


FIG. 2G



STATE OF OTQ AND LS IF TWO ENTRIES ARE FLUSHED:

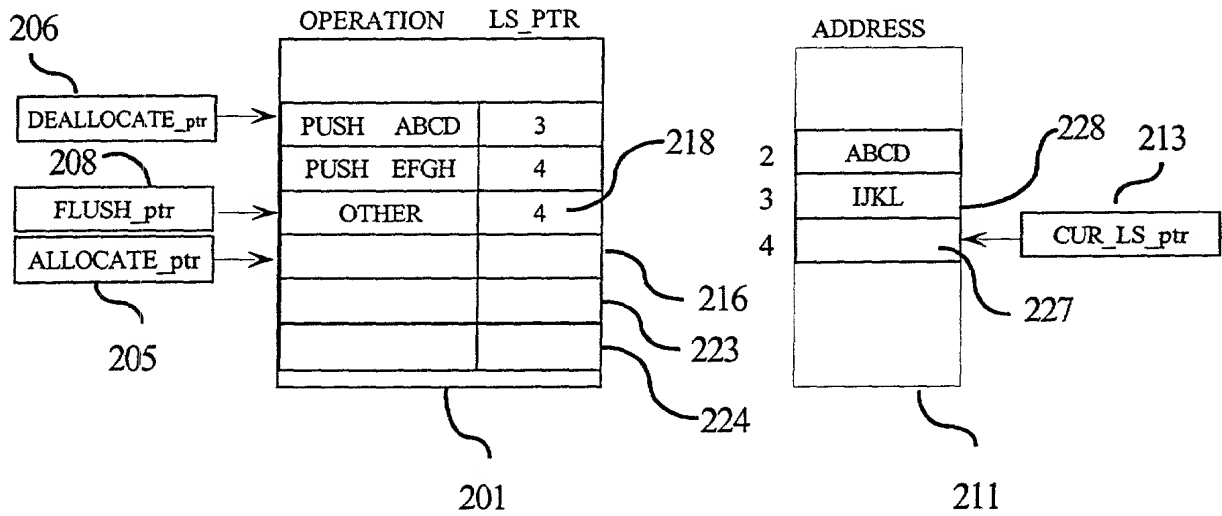


FIG. 2H

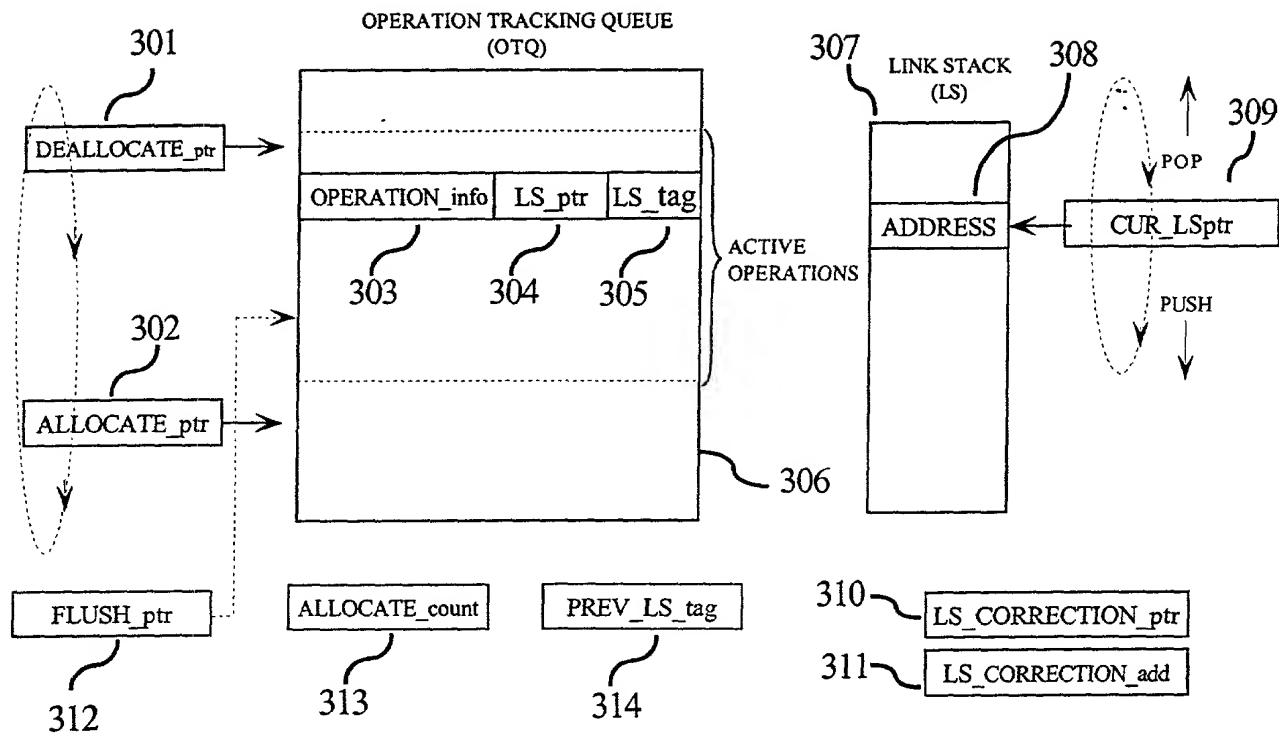


FIG. 3A

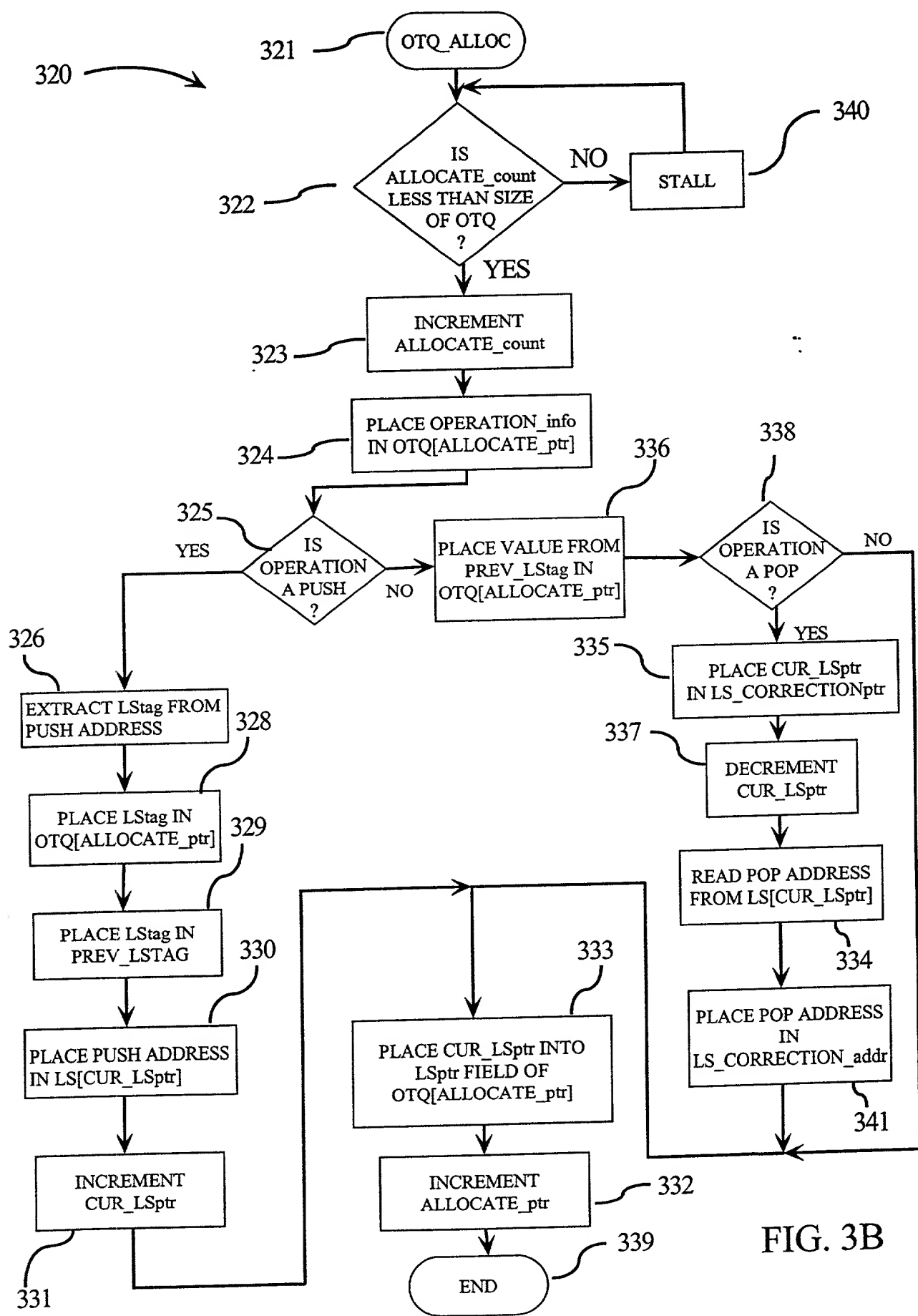


FIG. 3B

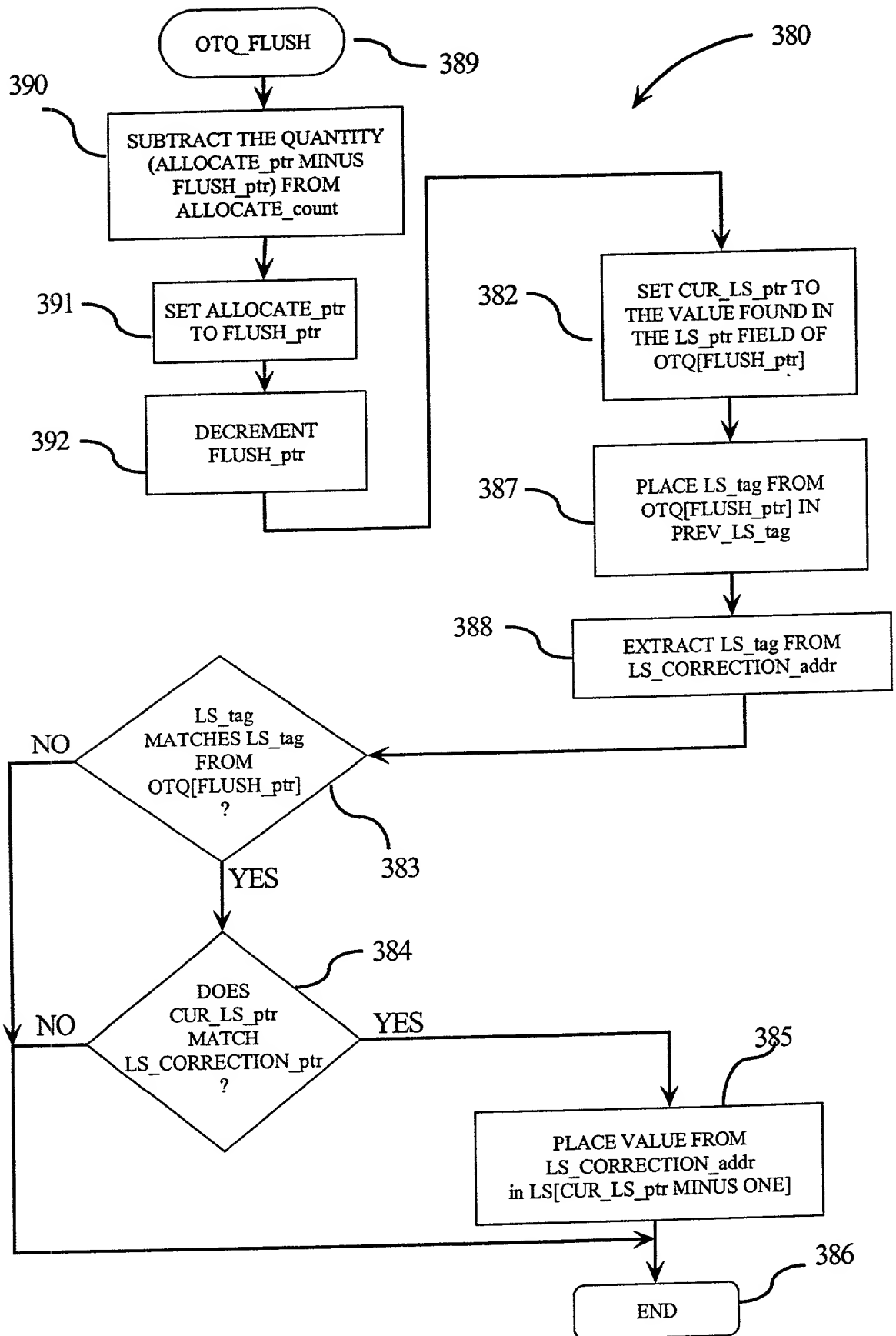


FIG. 3C

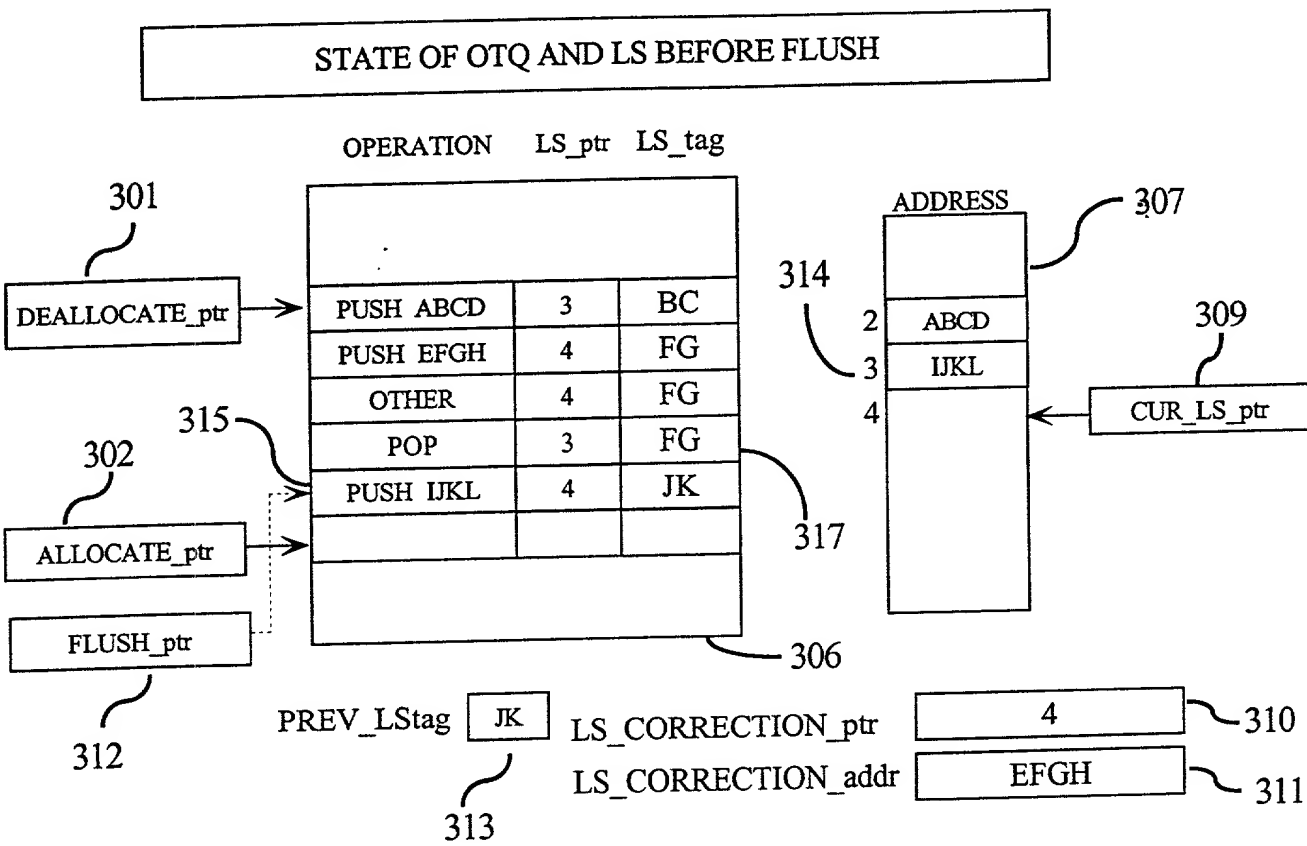


FIG. 3D

STATE OF OTQ AND LS IF ONE ENTRY IS FLUSHED

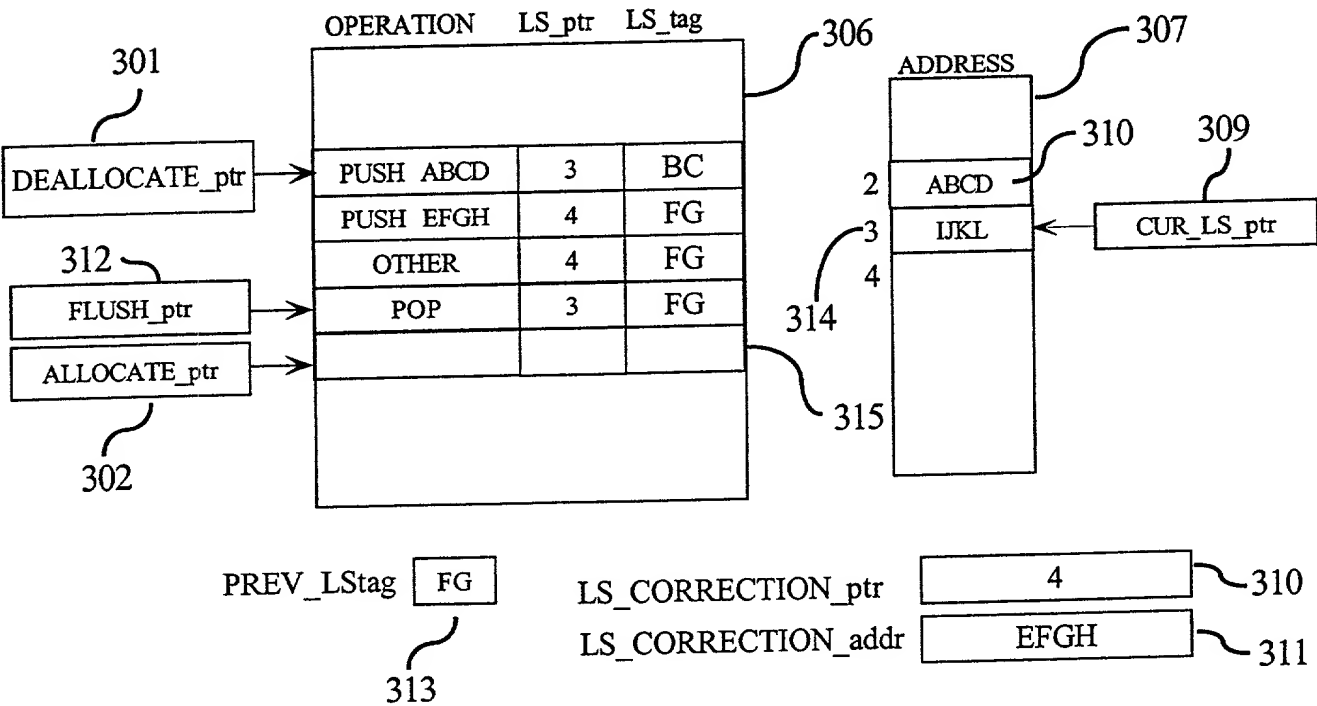


FIG. 3E

STATE OF OTQ AND LS IF TWO ENTRIES ARE FLUSHED

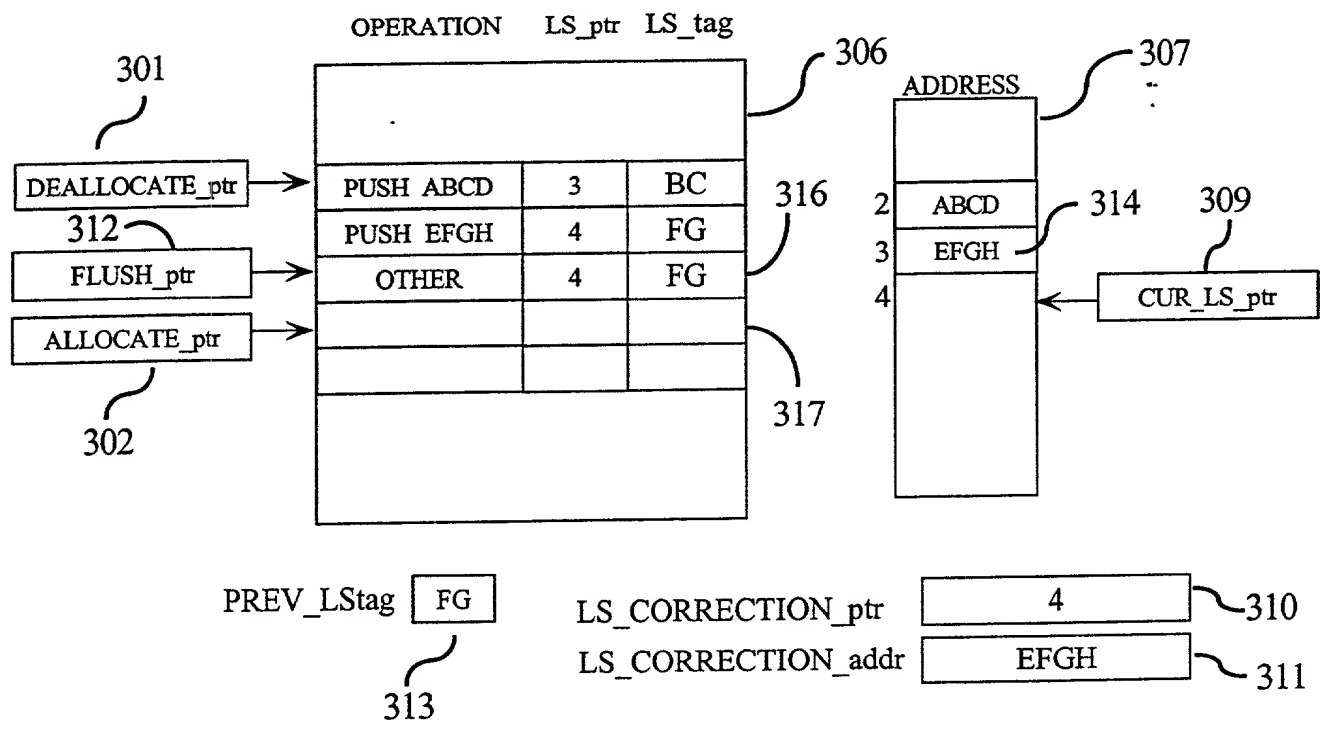
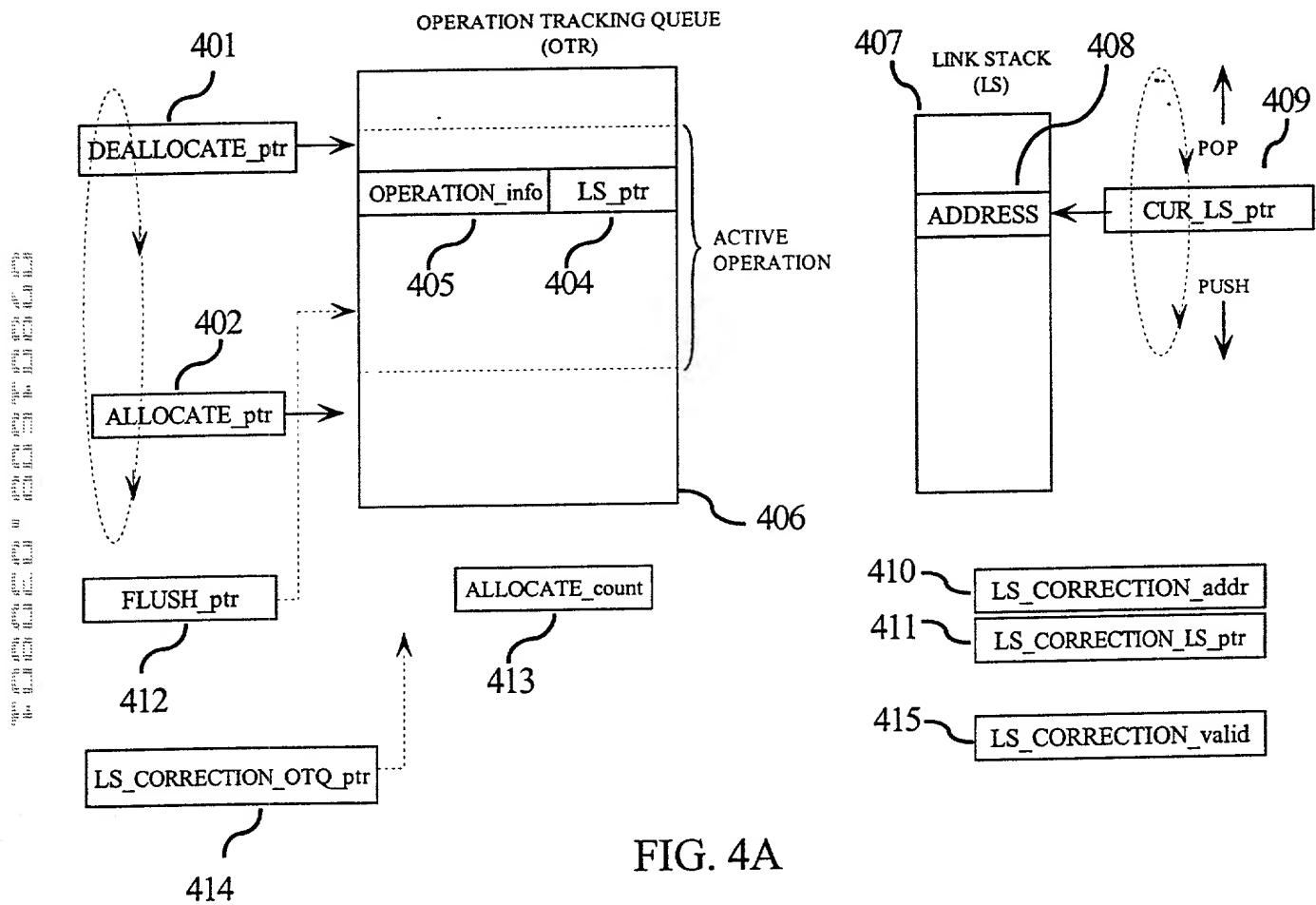


FIG. 3F





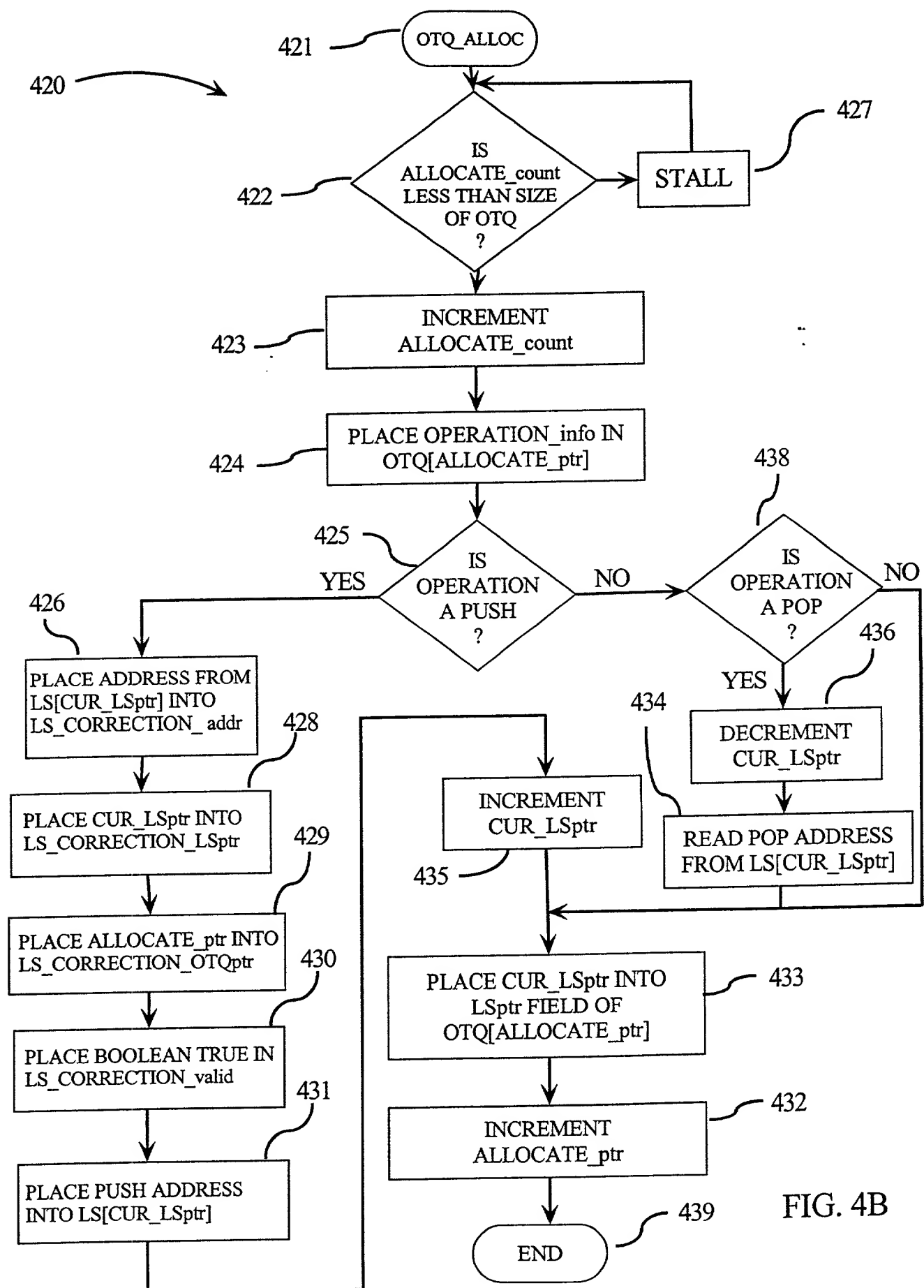


FIG. 4B

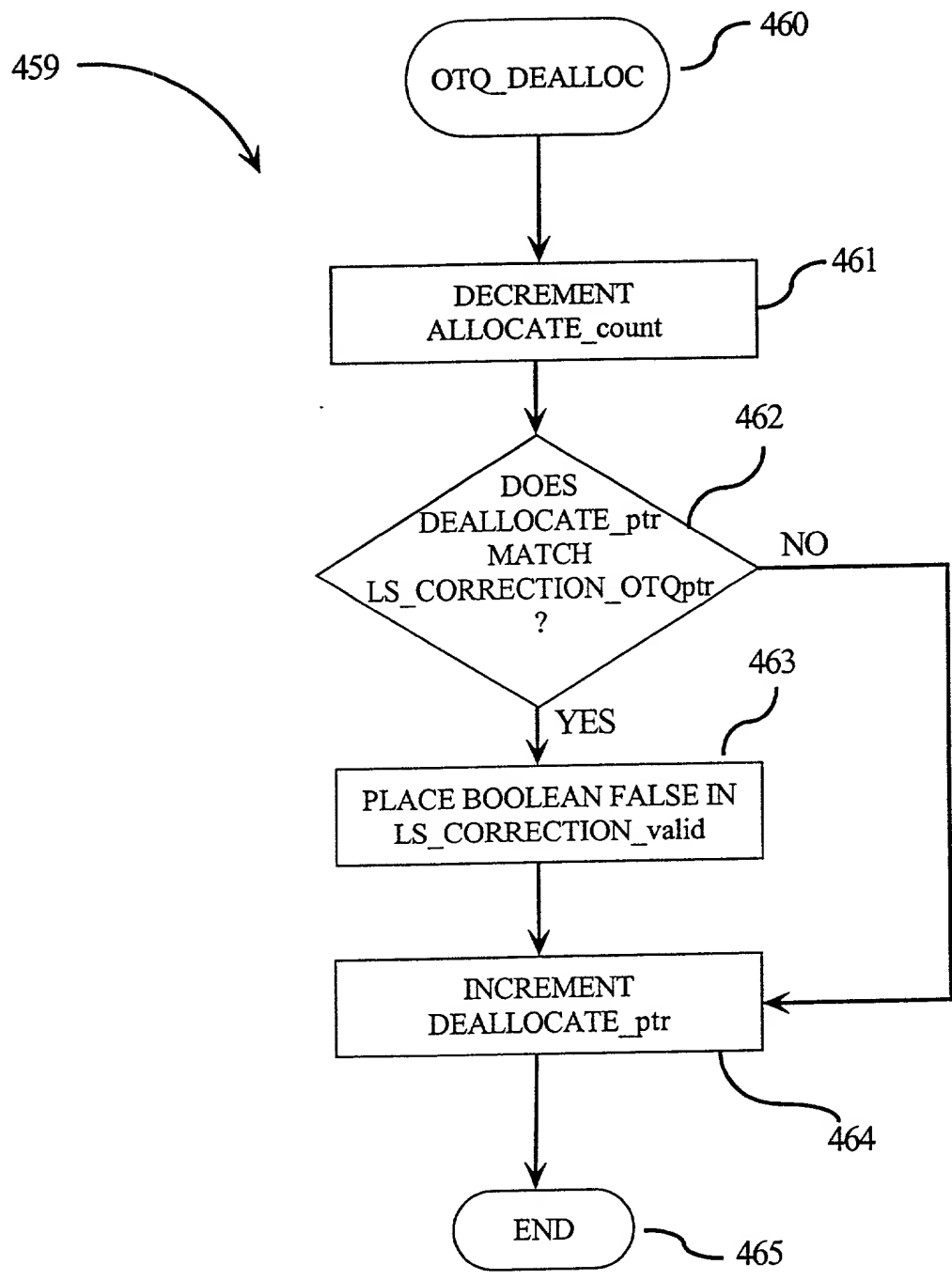


FIG. 4C

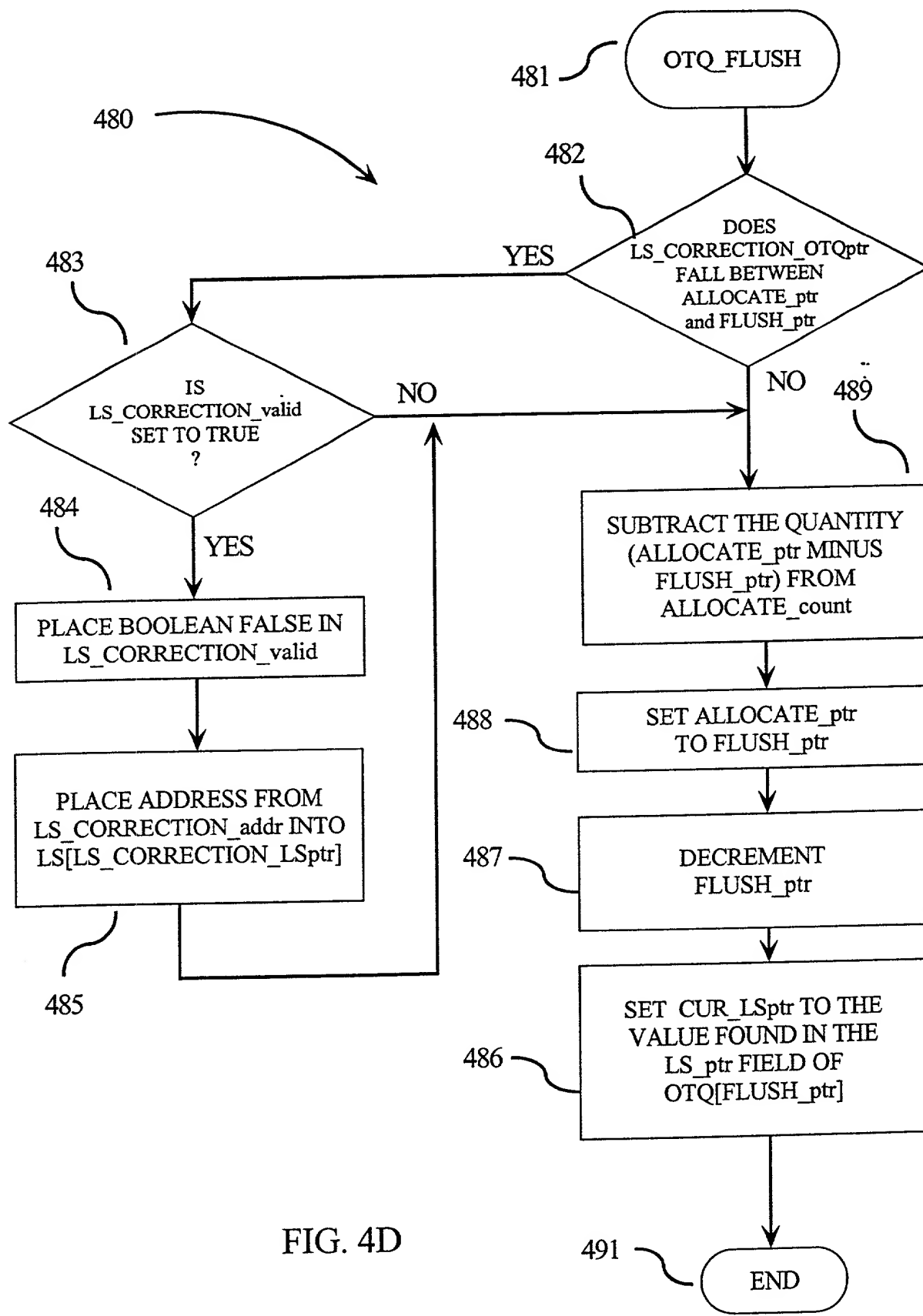


FIG. 4D

20/22 (

STATE OF OTQ AND LS BEFORE FLUSH:

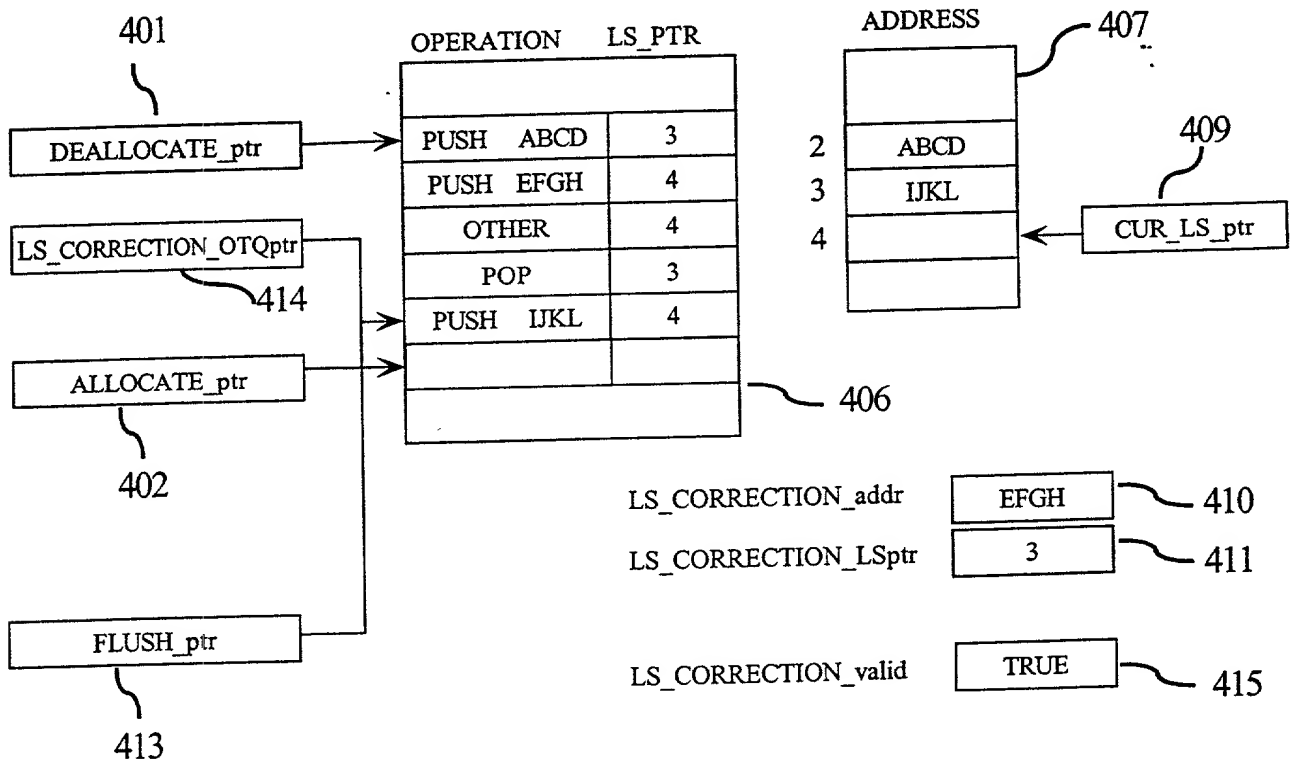


FIG. 4E

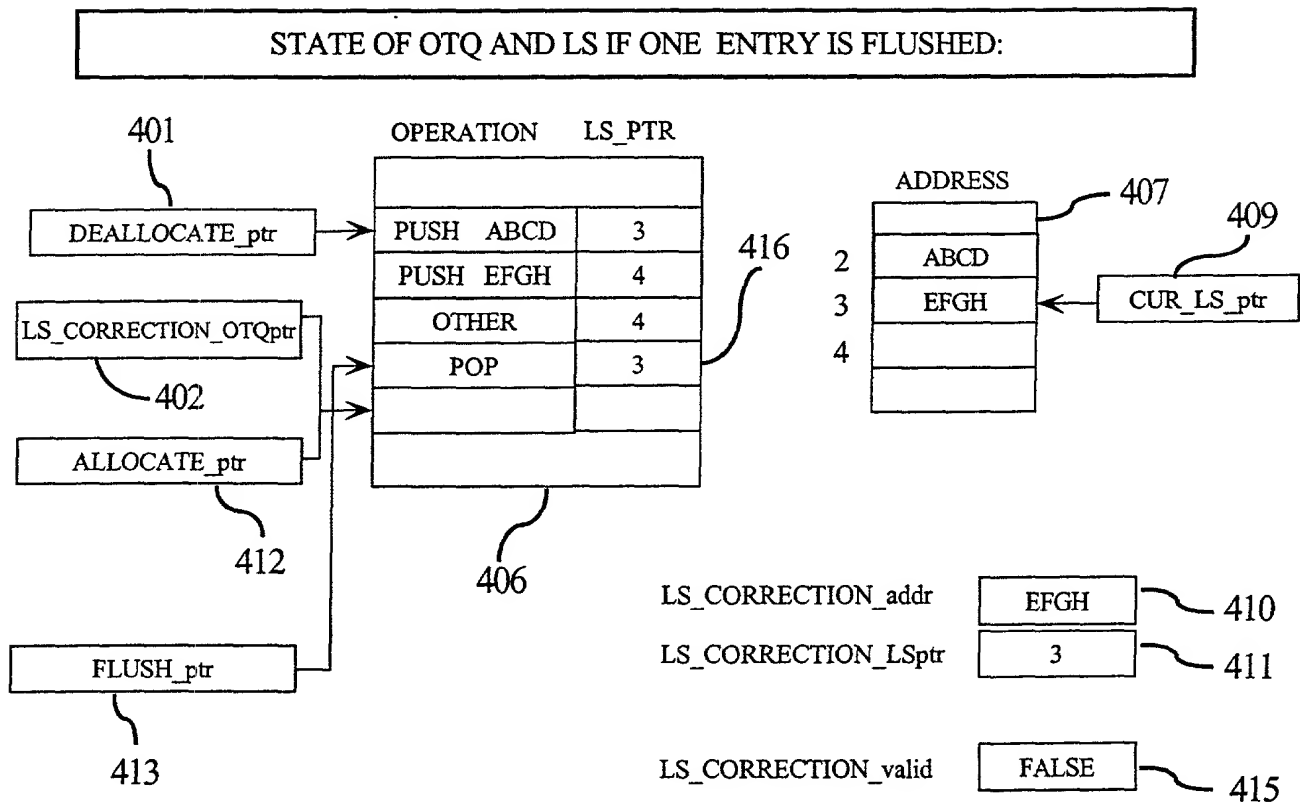


FIG. 4F

STATE OF OTQ AND LS IF TWO ENTRIES ARE FLUSHED:

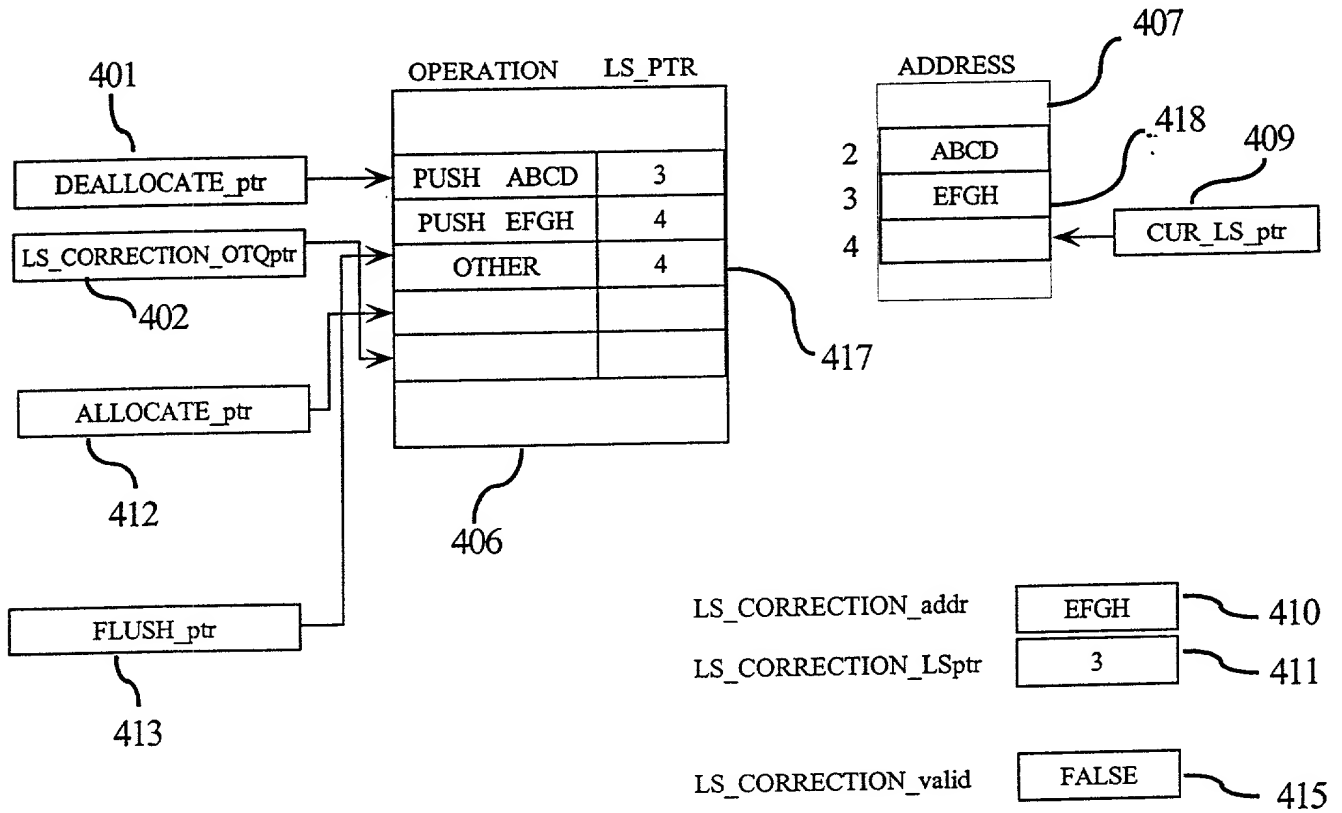


FIG. 4G